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a # # Added by Christiaan Simons # ## End of this file 1. Field of the Invention The present invention relates to a method of producing a semiconductor device, and more specifically to a method of producing a semiconductor device including a step of forming a metal pattern on a substrate. 2. Description of the Related Art In recent years, with the reduction in size and the increase in integration of semiconductor devices, the use of Cu (copper) as an interconnect material has been studied. In contrast, CVD (Chemical Vapor Deposition) methods are conventionally used to form a metal thin film. In a CVD method, however, a film is formed on the entire surface of a substrate at once, and therefore the film is formed at high temperatures. For this reason, polycrystalline films are formed at the start of film formation, and they become amorphous during film formation. The amorphous films cannot be used as a metal thin film. A technique of forming a metal thin film by sputtering has also been proposed. A film is formed on a substrate at room temperature using a sputtering apparatus. In this case, a metal target is used as a target material, and argon or another inert gas is used as a sputtering gas. However, when the sputtering apparatus is used to form a thin film on the entire surface of a substrate at once, the film is formed at low speeds. Therefore, a film is formed only on a small portion of the entire surface of a substrate at once in the case of forming the metal thin film by the sputtering method. For this reason, the film is formed at low speeds, and it is difficult to form a uniform thin film. When a sputtering apparatus is used to form a metal thin film on a substrate by dividing the substrate into small portions at once, the productivity of the metal thin film deteriorates. When a metal thin film is formed on a substrate by the sputtering method, the film is formed by the CVD method. For this reason, a sputtering apparatus is used in

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